sending an event signal to the remote interface;

setting a bit in a bit vector within the remote interface, wherein the setting of the bit corresponds to a specified type of system failure; and

notifying the remote computer that the event signal has been received by the remote interface.

REMARKS

Applicant has the following remarks in response to the Office Action.

Discussion of Rejections Under 35 U.S.C. § 112

In the Office Action, the Examiner rejected Claims 1-11 and 40-61 under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which application regards as the invention. Specifically, the Examiner took the position with respect to Claim 1 that the system interface lacks a reception of the failure information. Furthermore, the Examiner took the position with respect to Claim 40 that the phrase "server system" lacks antecedent basis and that the reporting limitation is not related to the other limitations. Applicant respectfully submits that these defects have been corrected by the above-amendments.

Discussion of Rejections Under 35 U.S.C. §§ 102(e) and 103(a)

In the Office Action, the Examiner rejected Claims 1, 3, 20, 40 and 44 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 5,815,652 to Ote, et al. Furthermore, the Examiner rejected Claims 12, 14, 19, and 29 under 35 U.S.C. § 103(a) as being unpatentable over Ote in view of U.S. Patent No. 5,708,775 to Nakamura. Claims 2, 4-11, 13, 15-18, 21, 22, 24-28, 30-33, 35-39, 41-61 were rejected under 35 U.S.C. § 103(a) as being obvious over Ote, Nakamura, and the knowledge of one of ordinary skill in the art. Claim 34 was rejected under 35 U.S.C. § 102(e) as being anticipated Nakamura.

Claims 1-33 and 35-61

Applicant encloses herewith a Declaration under 37 C.F.R. § 1.131 to overcome the Ote patent. Ote was either the single or principal reference cited by the Examiner with respect to each of Claims 1-33 and 35-61. Applicant has removed Ote as a reference by the filing of the §

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1.131 Declaration, Applicant submits that the remainder of the references do not, either in isolation or in combination, teach the invention as claimed.

Furthermore, Applicant respectfully traverses the rejections based upon the knowledge of one ordinary skill in the art. According to the Manual of Patent Examining Procedure, if an applicant traverses a rejection based upon the knowledge of ordinary skill in the art, the Examiner should cite a reference in support of his or her position. M.P.E.P. § 2144.03.

<u>Claim 34</u>

In the Office Action, the Examiner rejected Claim 34 as being anticipated by Nakamura. Nakamura teaches a fault notification system wherein a plurality of server units (13) monitor themselves for faults and transmit the fault information to a manager unit (12) that is registered with the server units (13). The Nakamura system stores fault information in a log file (53). However, Applicant respectfully submits that under selected error conditions, the Nakamura system is unable to monitor and store the fault information due to the severity of the fault. Applicant respectfully submits that if the processor or the processor bus of Nakamura fails, all software activity on the Nakamura system will halt.

In contrast, according to Applicant's invention, a plurality of microcontrollers that each are interconnected via a control bus are used to monitor and manage a computer. With respect to Claim 34, Applicant recites the elements of <u>means for generating and transmitting failure information related to the failure condition across a control bus from a first microcontroller to a system recorder microcontroller</u> and <u>means for accessing the system log via the system recorder microcontroller</u>.

According to Applicant's invention, a first microcontroller on the system bus is used to monitor system conditions such as the system temperature, system power, peripheral card detection, and processor faults. In the event of a failure condition, failure information is transmitted from the first microcontroller to a system recorder microcontroller which stores the failure information in a system log.

Applicant respectfully submits that Nakamura does not teach or suggest providing a plurality of microcontrollers connected to a control bus to manage and store failure information. Accordingly, Applicant respectfully submits that the Examiner's rejection based upon Nakamura has been overcome and Claim 34 is in condition for allowance.
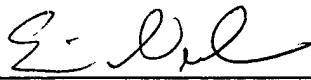
SUMMARY

Claims 1-61 are pending for examination. Applicant has endeavored to address all of the Examiner's concerns as expressed in the outstanding Office Action. If the Examiner has any questions which may be answered by telephone, he is invited to call the undersigned directly.

Respectfully submitted,

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Dated: 2/24/2000

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